

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| Inventor(s): | Nicholas Carl Seroff | Examiner: | |
| Serial No.: | | Group Art Unit: | |
| Filed: | April 1, 2004 | Docket No.: | STL11398 |
| Title: | Method And Apparatus For Obtaining Trace Data Of A High Speed Embedded Processor | | |

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| Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 | |
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INFORMATION DISCLOSURE STATEMENT

Section 1. Forms PTO/SB/08A and 08B (substitute for Form PTO-1449)

Form PTO/SB/08A /and 08B is attached hereto.

Section 2. Remarks or Additional Statements

☒ This Information Disclosure Statement is being filed along with a new patent application.

☐ This Information Disclosure Statement is being filed *within* three months of the filing date of the application or date of entry into the national stage of an international application or *before* the mailing date of a first Office action on the merits, whichever event occurs last. 37 C.F.R. 1.97(b).

☐ 37 C.F.R. §1.97(c) This Information Disclosure Statement is being filed *after* three months of the filing date of this national application or the date of entry of the national Stage as set forth in Section 1.491 in an international application or after the mailing date of the first Office Action on the merits, whichever event occurred last but *before* the mailing date of either:

1. a final action under section 1.113; or
2. a notice of allowance under section 1.311
whichever occurs first.

☐ A fee under 37 C.F.R. §1.17(p) is required because no certification under 37 C.F.R. §1.97(e) has been made; or

☐ No fee is required because certification statement under 37 C.F.R. §1.97(e) is noted below.

☐ 37 C.F.R. §1.97(d) This Information Disclosure Statement is being filed *after* a final action under §1.113, or a notice of allowance under §1.113, whichever occurs first, but before, or simultaneously with, the payment of the issue fee.

☐ A fee under 37 C.F.R. §1.17(p) is required and either of the statements below (37 C.F.R. §1.97(e)).

- ☐ 37 C.F.R. §1.97(e) Choose either Statement:
- ☐ Each item of information contained in the IDS was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three (3) months prior to the filing; or
- ☐ No item of information contained in the IDS was cited in a communication from a foreign patent office in the counterpart foreign application, and to my knowledge no item of information contained in the IDS was known to any individual designated in §1.56(c) more than three (3) months prior to the filing of this IDS.

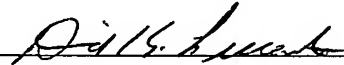
Consideration of the listed items is respectfully requested.

Respectfully submitted,

SEAGATE TECHNOLOGY LLC
(Assignee of Entire Interest)

Date

4/1/04



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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet

1

of

2

Complete if Known

Application Number

Filing Date

04/01/2004

First Named Inventor

Nicholas Carl Seroff

Art Unit

Examiner Name

Attorney Docket Number

STL11398

U. S. PATENT DOCUMENTS

| Examiner Initials* | Cite No. ¹ | Document Number | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
|--------------------|-----------------------|--|--------------------------------|--|---|
| | | Number-Kind Code ² (if known) | | | |
| | AA | US- 4,156,796 | 05/29/1979 | O'Neal et al. | |
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FOREIGN PATENT DOCUMENTS

| Examiner Initials* | Cite No. ¹ | Foreign Patent Document | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | T ⁶ |
|--------------------|-----------------------|---|--------------------------------|--|---|----------------|
| | | Country Code ³ Number ⁴ Kind Code ⁵ (if known) | | | | |
| | AA | EP 0610581 A2 | 08/17/1994 | IBM | | |
| | AB | EP 0814404 B1 | 12/29/1997 | Matsushita Electric Industrial | | |
| | AC | EP 0974093 B1 | 01/26/2000 | Advanced Micro Devices Inc. | | |
| | AD | JP 3-127236 | 05/30/1991 | | | |
| | AE | JP 7-21062 | 01/24/1995 | | | |

Examiner
SignatureDate
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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(Use as many sheets as necessary)

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| Sheet | 2 | of | 2 |
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|------------------------|----------------------|
| Application Number | |
| Filing Date | 04/01/2004 |
| First Named Inventor | Nicholas Carl Seroff |
| Art Unit | |
| Examiner Name | |
| Attorney Docket Number | STL11398 |

U. S. PATENT DOCUMENTS

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| Examiner Initials* | Cite No. ¹ | Foreign Patent Document | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | T ⁶ |
|--------------------|-----------------------|---|--------------------------------|--|---|----------------|
| | | Country Code ³ -Number ⁴ -Kind Code ⁵ (if known) | | | | |
| | AF | JP 11-96130 | 04/09/1999 | | | |
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| Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) | | | | Complete if Known | |
| | | | | Application Number | |
| | | | | Filing Date | 04/01/2004 |
| | | | | First Named Inventor | Nicholas Carl Seroff |
| | | | | Art Unit | |
| | | | | Examiner Name | |
| Sheet | 1 | of | 1 | Attorney Docket Number | STL11398 |

| NON PATENT LITERATURE DOCUMENTS | | | |
|---------------------------------|-----------------------|---|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | AA | NATIONAL SEMICONDUCTOR CORPORATION, "SCAN9210123 and SCAN921224 20-66 MHz Bus LVDS Serializer & Deserializer With IEEE 1149.1 (JTAG) And At-Speed BIST," National Semiconductor, (April 4, 2001). | |
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| Examiner Signature | | Date Considered | |
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